

## **1200V Silicon Carbide MOSFET**

### **Features**

- Robust semiconductor material 1200V SiC
- IGBT compatible driving function
- Very good temperature related stability
- $75m\Omega$  R<sub>DS\_ON</sub> @ V<sub>GS</sub>=20V
- High avalanche ruggedness
- JEDEC Qualified
- RoHS Compliant and Halogen Free

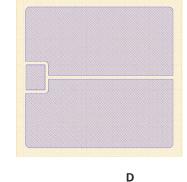
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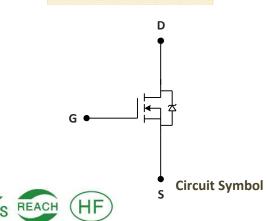
- Solar inverters
- PFC
- Motor Drives
- High Voltage DC-DC Converter
- Induction Heating and Welding
- EV Charging
- Switch mode power supplies, UPS

## **Ordering Information**

Part Number	Die Size (mm)			
PSMA1PVA2R75W	2.8*2.8			







### **Absolute Maximum Ratings**

T<sub>J</sub>=25°C, unless otherwise specified.

Symbol	Parameter	Rating	
V <sub>DSS</sub>	Drain-to-Source Voltage	1200V	
V <sub>GSS</sub>	Gate-to-Source Voltage	-10V/25V	
V <sub>GSS_OP</sub>	Recommended Operational	-5V/20V	
ID	Continuous Drain Current @ V <sub>GS</sub> =20V	35A	
	Continuous Drain Current @ V <sub>GS</sub> =20V (T <sub>C</sub> =100°C)	28A	
IDP	300us Pulsed Drain Current @ V <sub>GS</sub> =20V		
Is	Diode Continuous Forward Current	35A	

These are stress ratings only and functional operation is not implied. Exposure to absolute maximum ratings for prolonged time periods may affect device reliability.



# **Electrical Specifications**

T<sub>J</sub>=25°C, unless otherwise specified.

Symbol	Parameter	Test Condition	Min	Тур	Max	Unit
DC Chara	octeristics	1			ı	1
BV <sub>DSS</sub>	Drain-to-Source Breakdown Voltage	V <sub>GS</sub> =0V, I <sub>D</sub> =1mA	1200			V
l <sub>DSS</sub> Drain-to-S		V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V		0.1	10	uA
	Drain-to-Source Leakage Current	V <sub>DS</sub> =1200V, V <sub>GS</sub> =0V (T <sub>J</sub> =175°C)		1		
I <sub>GSS</sub>	Gate-to-Source Leakage Current	V <sub>GS</sub> =20V, V <sub>DS</sub> =0V		10	100	- nA
		V <sub>GS</sub> =-5V, V <sub>DS</sub> =0V		-10	-100	
R <sub>DS_ON</sub> Station	Static Drain-to-Source On-Resistance	V <sub>GS</sub> =20V, I <sub>D</sub> =35.6A		75	95	mΩ
		V <sub>GS</sub> =20V, I <sub>D</sub> =17.8A		72	90	
	Static Brain to Source on Resistance	V <sub>GS</sub> =20V, I <sub>D</sub> =20A (T <sub>J</sub> =175°C)	-	120	-	
V <sub>GS_TH</sub> _ (	Gate Threshold Voltage	V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =17.8mA	2	3.0	4	V
		V <sub>DS</sub> =V <sub>GS</sub> , I <sub>D</sub> =17.8mA (T <sub>J</sub> =175°C)	-	2.1	-	
Body Dio	de Characteristics					
V <sub>SD</sub>	Diode Forward Voltage	I <sub>F</sub> =35.6A, V <sub>GS</sub> =-5V		4.0		V

<sup>1.</sup> Pulse test; pulse width≤300µs, duty cycle≤2%.

<sup>2.</sup> All voltages are with respect to ground.

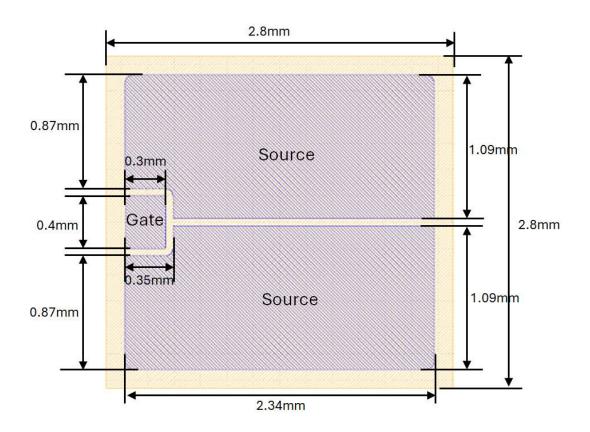
<sup>3.</sup> The Rds\_on characteristics were tested with the parts assembled in To-247-3L package



## **Mechanical Parameters**

Parameter	Typical Value	Unit	
Die Dimensions (L x W)	2.8*2.8	mm	
Gate Pad Dimensions (L x W)	293*398	μm	
Die Thickness	175 ± 20	μm	
Top Side Source metallization (AlCu)	4.0	μm	
Top Side Gate metallization (AlCu)	4.0	μm	
Bottom Drain metallization (Ti/Ni/Ag)	0.1/0.3/1	μm	
Cut line	100	μm	

# **Chip Dimensions**







#### **NOTICE**

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